L Number	Hits	Search Text	DB	Time stamp
1	2643	(test\$3 simulat\$3 run\$4) with (component parts parameter function) with (circuit	USPAT; US-PGPUB	2004/09/08
2	2	chip IC) with (order sequen\$6) ((test\$3 simulat\$3 run\$4) with (component parts parameter function) with (circuit chip IC) with (order sequen\$6)) and	USPAT; US-PGPUB	2004/09/08 13:15
3	947	(345/771.ccls.) ((test\$3 simulat\$3 run\$4) with (component parts parameter function) with (circuit chip IC) with (order sequen\$6)) and (interface)	USPAT; US-PGPUB	2004/09/08
4	32	((test\$3 simulat\$3 run\$4) with (component parts parameter function) with (circuit chip IC) with (order sequen\$6)) and ("graphical user interface" GUI)	USPAT; US-PGPUB	2004/09/08 13:17
5	1455	716/4.ccls.	USPAT; US-PGPUB	2004/09/08
6	12	716/4.ccls. and (display\$3 with (list netlist\$3 net-list\$3 "net list" "net listing") with (part component parameter cell))	USPAT; US-PGPUB	2004/09/08 14:12
	202		USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:10
-	4	((345/771).ccls.) and (test\$3 with (circuit IC chip) with (design))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:12
-	4	(((345/771).ccls.) and (test\$3 with (circuit IC chip) with (design))) and ("netlist" net-list "net list")	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:13
_	0	"1198" and ((display\$3) with ("netlist" net-list "net.list") with (component cell function))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:14
_	0	"1198" and ((display\$3) with ("netlist" net-list "net list" list) with (component cell function))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:14
-	0	1	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:20
_	14		USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:14
-	0	1 1 2	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:21
-	47	circuit chip IC component cell function)) ((display\$3) with ("netlist" net-list "net list" list) with (circuit electrical IC chip design) with (component cell	USPAT; US-PGPUB; EPO; JPO	2004/08/31
	36	function)) and (test\$3 with (design circuit chip IC component cell function)) (((display\$3) with ("netlist" net-list "net list" list) with (circuit electrical IC chip design) with (component cell	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:54
	38	<pre>function)) and (test\$3 with (design circuit chip IC component cell function))) and (interface GUI 345/\$.ccls.)</pre>	USPAT;	2004/08/31
	38	component function block resistor transistor) with (display\$3) with (netlist\$3 net-list\$3 "net list" "net	US-PGPUB; EPO; JPO	13:57
_	2	listing" list) with (test\$3) (select\$3 choos\$3 pick\$3) with (button icon) with (cell component function block resistor transistor) with (display\$3) with (netlist\$3 net-list\$3 "net list" "net listing" list) with (test\$3)	USPAT; US-PGPUB; EPO; JPO	2004/08/31 13:53
	l	THE TESTING TEST WICH (CESCAS)	1	J

-	33	((select\$3 choos\$3 pick\$3) with (cell	USPAT;	2004/08/31
	1	component function block resistor	US-PGPUB;	13:58
		transistor) with (display\$3) with	EPO; JPO	
		<pre>(netlist\$3 net-list\$3 "net list" "net listing" list) with (test\$3)) and</pre>		•
		(interface GUI 345/\$.ccls.)		
_	33	(((select\$3 choos\$3 pick\$3) with (cell	USPAT;	2004/08/31
	33	component function block resistor	US-PGPUB;	13:55
		transistor) with (display\$3) with	EPO; JPO	
		(netlist\$3 net-list\$3 "net list" "net		
		listing" list) with (test\$3)) and		
		(interface GUI 345/\$.ccls.)) not		
		<pre>((((display\$3) with ("netlist" net-list "net list" list) with (circuit electrical</pre>		
		IC chip design) with (component cell		
		function)) and (test\$3 with (design		
		circuit chip IC component cell		
		function))) and (interface GUI		
		345/\$.ccls.))		
-	0	(select\$3 choos\$3 pick\$3) with (cell	USPAT;	2004/08/31
		component function block resistor	US-PGPUB;	13:58
1		transistor) with (circuit chip IC) with (display\$3) with (netlist\$3 net-list\$3	EPO; JPO	
		"net list" "net listing" list) with		
		(test\$3)		
	24	((select\$3 choos\$3 pick\$3) with (cell	USPAT;	2004/08/31
		component function block resistor	US-PGPUB;	13:58
		transistor) with (display\$3) with	EPO; JPO	
		(netlist\$3 net-list\$3 "net list" "net		
		listing" list) with (test\$3)) and		
	23	<pre>(circuit chip IC) (((select\$3 choos\$3 pick\$3) with (cell</pre>	USPAT;	2004/08/31
-	23	component function block resistor	US-PGPUB;	14:55
		transistor) with (display\$3) with	EPO; JPO	
		(netlist\$3 net-list\$3 "net list" "net		
		listing" list) with (test\$3)) and		
		(circuit chip IC)) and (interface GUI		
	0.00	345/\$.ccls.)	ucbam.	2004/08/31
-	202	(345/771).ccls.	USPAT; US-PGPUB;	14:55
			EPO; JPO	11.55
-	38	((345/771).ccls.) and (test\$3 with	USPAT;	2004/08/31
1		(parameter component function block	US-PGPUB;	14:56
		cell))	EPO; JPO	
-	191	((345/771).ccls.) and (user (select\$5	USPAT;	2004/08/31
		choos\$3 pick\$3 set\$4) with test\$3 with	US-PGPUB;	14:56
ļ		(parameter component function block cell))	EPO; JPO	
_	5	1 ' '	USPAT;	2004/08/31
		(select\$5 choos\$3 pick\$3 set\$4) with	US-PGPUB;	14:58
		test\$3 with (parameter component function	EPO; JPO	
		block cell))		0004/00/55
-	19	1 , , , , , , , , , , , , , , , , , , ,	USPAT;	2004/08/31
		(list netlist\$3 net-list\$3 "net list"	US-PGPUB;	15:03
		<pre>"net listing") with (component parameter block cell function))</pre>	EPO; JPO	
_	4		USPAT;	2004/08/31
	•	(list netlist\$3 net-list\$3 "net list"	US-PGPUB;	15:04
		"net listing") with (component parameter	EPO; JPO	
		block cell function))) and (test\$3 with		
		(circuit IC chip design block cell		
	007	function component parameter))	IICDAM.	2004/09/07
	927	(345/771 345/744 345/738).ccls.	USPAT; US-PGPUB;	2004/09/07
			EPO; JPO;	05.23
[			DERWENT	
	149		USPAT;	2004/09/07
		(display\$3 with (cell component part pin	US-PGPUB;	09:38
	Ī	parameter) with (name number list\$3	EPO; JPO;	
, 1		netlist\$3 net-list\$3 "net list"))	DERWENT	

_	1	((345/771 345/744 345/738).ccls.) and (display\$3 with (cell component part pin	USPAT; US-PGPUB;	2004/09/07 09:27
		<pre>parameter) with (name number list\$3 netlist\$3 net-list\$3 "net list") with (circuit IC chip))</pre>	EPO; JPO; DERWENT	
_	39	(((345/771 345/744 345/738).ccls.) and (display\$3 with (cell component part pin	USPAT; US-PGPUB;	2004/09/07 09:28
		parameter) with (name number list\$3 netlist\$3 net-list\$3 "net list")) and	EPO; JPO; DERWENT	
		((test\$3 run\$3 debug\$4 de-bug\$4 simulat\$4) with (component parameter cell		
_	14	part pin)) (((345/771 345/744 345/738).ccls.) and	USPAT;	2004/09/07
:		(display\$3 with (cell component part pin parameter) with (name number list\$3	US-PGPUB; EPO; JPO;	09:40
		netlist\$3 net-list\$3 "net list")) ) and ((test\$3 run\$3 debug\$4 de-bug\$4 simulat\$4) with (component parameter cell	DERWENT	
		part pin) with (button icon item element object graphic\$3))		
_	3632		USPAT; US-PGPUB;	2004/09/07 09:47
		netlist\$3 net-list\$3 "net list") with (circuit IC chip electric\$3))	EPO; JPO; DERWENT	0004/00/05
_	87	((display\$3 with (cell component part pin parameter) with (name number list\$3 netlist\$3 metlist\$3 "net list") with	USPAT; US-PGPUB; EPO; JPO;	2004/09/07 09:47
		(circuit IC chip electric\$3)) ) and ((test\$3 run\$3 debug\$4 de-bug\$4	DERWENT	0.0
		simulat\$4) with (component parameter cell part pin) with (button icon select\$3))		
-	86	(((display\$3 with (cell component part pin parameter) with (name number list\$3	USPAT; US-PGPUB;	2004/09/07 09:40
		netlist\$3 net-list\$3 "net list") with (circuit IC chip electric\$3)) ) and ((test\$3 run\$3 debug\$4 de-bug\$4	EPO; JPO; DERWENT	
		simulat\$4) with (component parameter cell part pin) with (button icon select\$3)))		
		not ((((345/771 345/744 345/738).ccls.) and (display\$3 with (cell component part		
		<pre>pin parameter) with (name number list\$3 netlist\$3 net-list\$3 "net list")) ) and</pre>		
		((test\$3 run\$3 debug\$4 de-bug\$4 simulat\$4) with (component parameter cell part pin) with (button icon item element		
_	52	object graphic\$3)))	USPAT;	2004/09/07
		pin parameter) with (name number list\$3 netlist\$3 net-list\$3 "net list") with	US-PGPUB; EPO; JPO;	09:42
		(circuit IC chip electric\$3)) ) and ((test\$3 run\$3 debug\$4 de-bug\$4	DERWENT	
		simulat\$4) with (component parameter cell part pin) with (button icon select\$3))) not ((((345/771 345/744 345/738).ccls.)		
		and (display\$3 with (cell component part pin parameter) with (name number list\$3		
		netlist\$3 net-list\$3 "net list")) ) and ((test\$3 run\$3 debug\$4 de-bug\$4		
		simulat\$4) with (component parameter cell part pin) with (button icon item element object graphic\$3)))) and (interface)		
L	<u> </u>	object graphitchalli and (interrace)	I	

-	6	(((display\$3 with (cell component part	USPAT;	2004/09/07
		pin parameter) with (name number list\$3 netlist\$3 net-list\$3 "net list") with	US-PGPUB; EPO; JPO;	09:42
		(circuit IC chip electric\$3))) and	DERWENT	
		((test\$3 run\$3 debug\$4 de-bug\$4	2211112112	
-		simulat\$4) with (component parameter cell		
		part pin) with (button icon select\$3)))		
		not ((((345/771 345/744 345/738).ccls.)		
		and (display\$3 with (cell component part		
		pin parameter) with (name number list\$3 netlist\$3 net-list\$3 "net list")) ) and		
		((test\$3 run\$3 debug\$4 de-bug\$4		
		simulat\$4) with (component parameter cell		
		part pin) with (button icon item element		
		object graphic\$3)))) and ("graphical user		
	245	interface" GUI)	HCDAM.	2004/09/07
_	245	<pre>(display\$3 with (cell component part pin parameter) with (list\$3 netlist\$3</pre>	USPAT; US-PGPUB;	2004/09/07 09:47
		net-list\$3 "net list") with (circuit IC	EPO; JPO;	
		chip electric\$3))	DERWENT	
-	2	((display\$3 with (cell component part pin	USPAT;	2004/09/07
		parameter) with (list\$3 netlist\$3	US-PGPUB;	09:48
		net-list\$3 "net list") with (circuit IC	EPO; JPO;	
		<pre>chip electric\$3)) ) and ((select\$3 choos\$3 chosen pick\$3 group\$3) with</pre>	DERWENT	
		(test\$3 run\$3 debug\$4 de-bug\$4 simulat\$4)		
		with (component parameter cell part pin)		
		with (button icon))		
-	4		USPAT;	2004/09/07
		pin parameter) with (list\$3 netlist\$3	US-PGPUB;	09:49
		net-list\$3 "net list") with (circuit IC chip electric\$3)) ) and ((select\$3	EPO; JPO; DERWENT	
		choos\$3 chosen pick\$3 group\$3) with	DERWENT	
		(test\$3 run\$3 debug\$4 de-bug\$4 simulat\$4)		
		with (component parameter cell part		
		pin))) and ("GUI" "graphical user		
		interface")		2004/00/07
-	26	((display\$3 with (cell component part pin parameter) with (list\$3 netlist\$3	USPAT; US-PGPUB;	2004/09/07
		net-list\$3 "net list") with (circuit IC	EPO; JPO;	09.50
		chip electric\$3)) and ((select\$3	DERWENT	
		choos\$3 chosen pick\$3 group\$3) with		
[		(test\$3 run\$3 debug\$4 de-bug\$4 simulat\$4)		
1		with (component parameter cell part pin))		0004/00/07
-	0	("114and(list\$3test\$3)").PN.	USPAT; US-PGPUB	2004/09/07
_	0	   (display\$3 with (component part parameter	USPAT;	2004/09/08
		cell) with (circuit IC chip) with (icon))	US-PGPUB	10:58
1	1	and (display\$3 with (component part		
	1	parameter cell) with (circuit IC chip)		
		with (color)) and ((test\$3 simulat\$4		
1	1	run\$4) with (parameter component part		
_	3	cell) with (order sequen\$6)) (display\$3 with (component part parameter	USPAT;	2004/09/08
_		cell) with (circuit IC chip) with (icon))	US-PGPUB	11:00
	1	and (display\$3 with (component part		
		parameter cell) with (circuit IC chip)		
		with (color))		2004/00/00
-	352		USPAT;	2004/09/08 11:00
		cell) with (icon)) and (display\$3 with	US-PGPUB	11.00
1		(component part parameter cell) with (color))		
_	6		USPAT;	2004/09/08
1		parameter cell) with (icon)) and	US-PGPUB	11:01
		(display\$3 with (component part parameter		
		cell) with (color))) and (345/771.ccls.)	TIGD# TI	2004/00/00
-	113		USPAT;	2004/09/08
		parameter cell) with (icon)) and (display\$3 with (component part parameter	US-PGPUB	11.09
		cell) with (color))) and (345/\$.ccls.)		
L	<del></del>	0011, WICH (00101//, dita (010/9.0010./	<u> </u>	1